

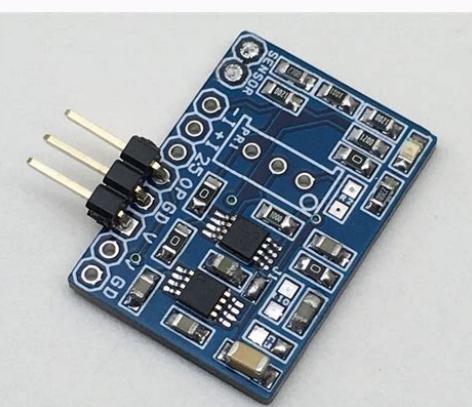
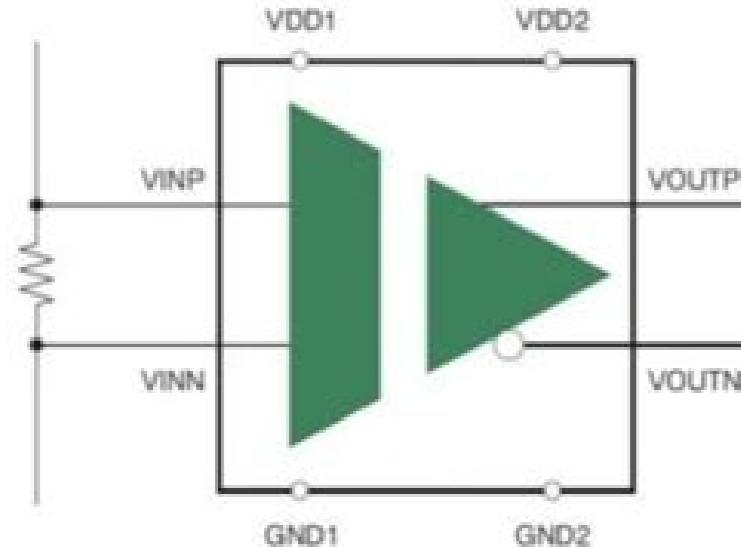


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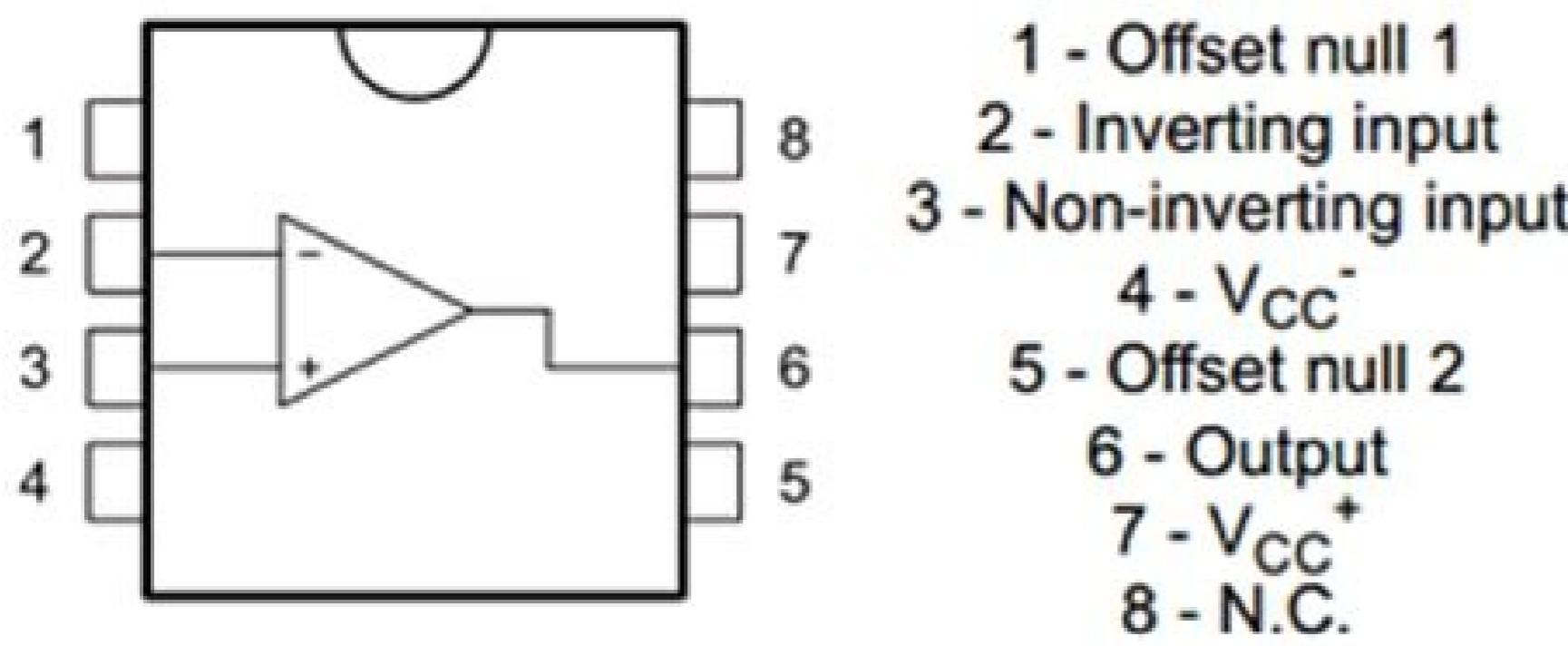


Next

## GENERAL PIN CONFIGURATION OF ISOLATION AMPLIFIER

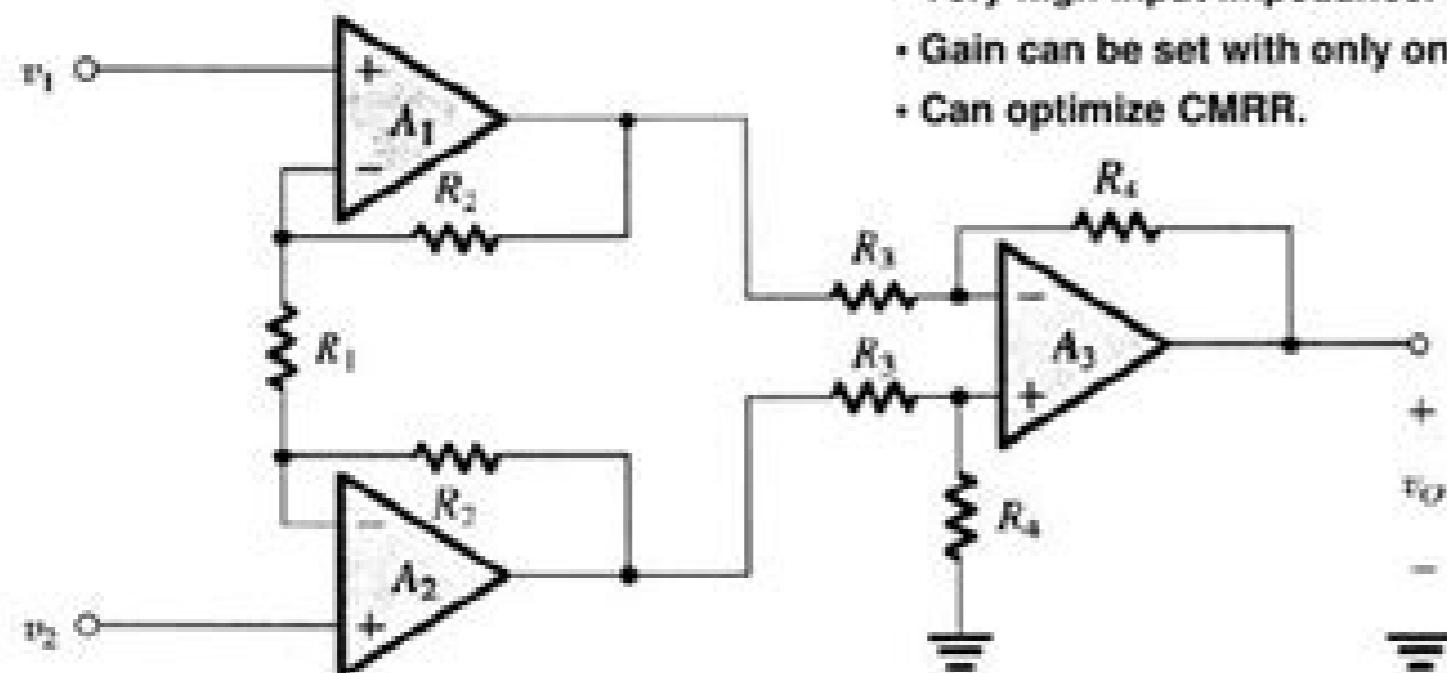


# UA741



## Instrumentation Amplifier

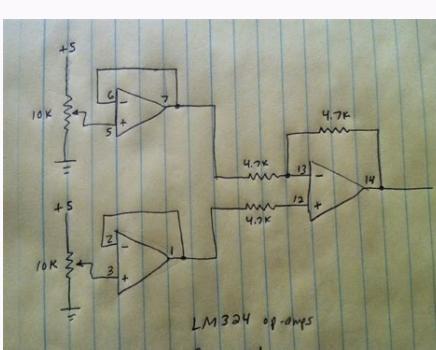
- Very high input impedance.
- Gain can be set with only one resistor.
- Can optimize CMRR.



$$A_V = -\frac{R_4}{R_3} \left( 1 + \frac{2R_2}{R_1} \right)$$

Source: Sedra, A. S., and Smith, K. C., "Microelectronic Circuits," Oxford, 1998.

For one-resistor gain adjust, set  $R_4 = R_3$  and fix  $R_2$ .



Mcq on instrumentation amplifier using op amp. Instrumentation amplifier using op amp experiment. Instrumentation amplifier using op amp tutorialspoint. Instrumentation amplifier using op amp ppt. Working of instrumentation amplifier using op amp. Instrumentation amplifier using 3 op amp. Instrumentation amplifier using op amp pdf. Instrumentation amplifier using op amp derivation.

**BACK TO TOP Transducer Bridge Instrumentation Amplifier** The resistive transducer bridge is a network of resistors whose resistance varies due to changes in some physical condition. High Slew Rate: The slew rate of the instrumentation amplifier must be as high as possible to provide maximum undistorted output voltage swing. This produces a definite input for the instrumentation amplifier and the output of the amplifier will no longer be zero. Light Intensity Meter: The same circuit can be used to detect variations in the intensity of light, by replacing the thermistor by a Light Dependent Resistor (LDR). The resistive bridge is kept balanced for a particular reference temperature when  $V_o = 0V$ . For example, Thermistors change their resistance with temperature and Light Dependent Resistors change their resistance to change in light intensity. Therefore, a good instrumentation amplifier has to meet the following specifications: Finite, Accurate and Stable Gain: Since the instrumentation amplifiers are required to amplify very low-level signals from the transducer device, high and finite gain is the basic requirement. The electrical signal is fed to an instrumentation amplifier. Thus, the potential at node H is also  $V_2$ . A special implementation of Operational Amplifiers is the Instrumentation Amplifier, a type of Differential Amplifier with Input Buffer Amplifier. The op-amp 3 is a difference amplifier that forms the output stage of the instrumentation amplifier. In this circuit, a non-inverting amplifier is connected to each input of the differential amplifier. The circuit diagram of an instrumentation amplifier is as shown in the figure below. The instrumentation amplifier is intended for precise, low-level signal amplification where high input resistance, low noise and accurate closed-loop gain is required. The potential at node D is the input voltage  $V_2$ . When the bridge is balanced, i.e. at some reference condition of the physical quantity being measured, we get,  $V_a = V_b RA(Vdc)/(RA+RT) = RB(Vdc)/(RB+RC)$  Under this condition, the differential input to the instrumentation amplifier is  $VDiff = V_b - V_a = 0$  Thus, the output of the amplifier is zero. Note: The overall voltage gain of an instrumentation amplifier can be controlled by adjusting the value of resistor  $R_{Gain}$ . A transducer is a device which converts one form of energy into another. The potential at node A is the input voltage  $V_1$ . The display can be calibrated in terms of the units of the physical quantity being measured. The input impedance of the instrumentation amplifier is dependent on the non-inverting amplifier circuit in the input stage. The gain also needs to be accurate and the closed-loop gain must be stable. Consider the input stage of the instrumentation amplifier as shown in the figure below. Hence the potential at node C is also  $V_2$ , from the virtual short. The effective resistance of the transducer device is  $RT \pm \Delta R$ . The general single ended amplifiers are not suitable for such operations. This causes the amplifier to produce a finite output, which in turn drives the meter. Low Output Impedance: The output impedance of a good instrumentation amplifier must be very low (ideally zero), to avoid loading effect on the immediate next stage. Consequently, the display device connected at the output displays the reference value of the physical quantity being measured. The resistive bridge is kept balanced for some reference temperature. High Input Impedance: To avoid the loading of input sources, the input impedance of the instrumentation amplifier must be very high (ideally infinite). The resistive bridge is supplied with a DC voltage,  $Vdc$ . The gain of the amplifier can be appropriately set to indicate the desired range of temperature. BACK TO TOP Applications of Instrumentation Amplifier: The instrumentation amplifier, along with a transducer bridge can be used in a wide variety of applications. The amplified signal is then fed to a display device, which is calibrated to detect the change in the quantity being measured. High CMRR: The output from the transducer usually contains common mode signals, when transmitted over long wires. Also, low power consumption, high slew rate and high common-mode rejection ratio are desirable for good performance. BACK TO TOP Requirements of a Good Instrumentation Amplifier: An instrumentation amplifier is usually employed to amplify low-level signals, rejecting noise and interference signals. BACK TO TOP Working of Instrumentation Amplifier: The overall stage of the instrumentation amplifier is a differential amplifier. The output signal  $Vout$  is the amplified difference of the input signals applied to its input terminals. The overall gain of the amplifier is given by the term  $(R3/R2)(2(R1+R_{Gain})/R_{Gain})$ . For the rejection of noise, amplifiers must have high common-mode rejection ratio. Thus, the CMRR of the instrumentation amplifier must be ideally infinite. Such an electrical signal can be amplified and used to monitor and control the physical process. BACK TO TOP Three Op-Amp Instrumentation Amplifier: The most commonly used instrumentation amplifiers consist of three op-amps. The resistances  $RB$  and  $RC$  are constant and hence the voltage  $VB$  remains same as before, i.e.  $Vb = RB(Vdc)/(RB+RC)$  But the voltage  $Va$  changes due to the change in resistance of the transducer device and is now given as,  $Va = RA(Vdc)/(RA+RT+ \Delta R)$  The differential voltage  $VDiff$  is,  $VDiff = Vb - Va$   $VDiff = (RB(Vdc)/(RB+RC)) - (RA(Vdc)/(RA+RT+ \Delta R))$  If all the resistances in the circuit are chosen to be of same value, i.e.  $RA = RB = RC = RT = R$   $VDiff = (R(Vdc)/2R) - (R(Vdc)/2R+ \Delta R)$   $VDiff = (R(Vdc)/2R) - (R(Vdc)/2R+ \Delta R)$   $VDiff = \Delta R(Vdc)/(2(2R+ \Delta R))$  If the value of  $VDiff$  is positive, it indicates that  $Vb$  is greater than  $Va$ . The output of the instrumentation amplifier is given as,  $VO = (R3/R2)Vd$   $- (R3/R2)[\Delta R(Vdc)/(2(2R+ \Delta R))]$  As the change in resistance  $\Delta R$



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